

Features

- Operating voltage: 3.5V~5.5V
- Differential input
- Power down control
- Bellcore CAS detection (Type II)
- 3.58MHz clock input
- Low standby current
- High sensitivity: -33dBm
- Data valid output
- 16-pin DIP/SOP package

Applications

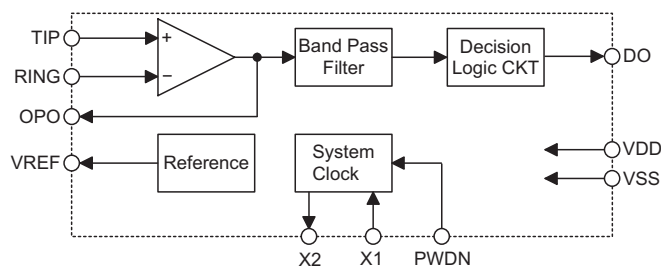
- Feature phone system
- Caller ID with Call Waiting
- Computer telephony interface products
- Analog Display Service Interface (ADSI)

General Description

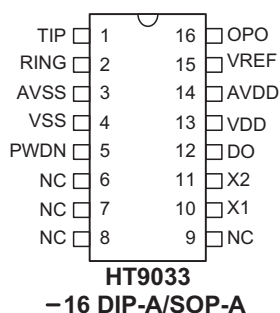
The HT9033 is a CPE Alerting Signal (CAS) tone receiver integrated with digital filter/detector and analog front end circuit. The HT9033 using analog filter techniques to achieve very high precision detection for the present of CAS tone.

It is ideal for receiving physical layer signal like Bellcore's CPE Alerting Signal (Type II caller ID) and similar evolving services like ETS/BT.

Block Diagram



Pin Assignment



Pin Description

Pin Name	I/O	Description
TIP	I	This pin is connected to the tip side of the twisted pair wires.
RING	I	This pin is connected to the ring side of the twisted pair wires.
OPO	O	Output of OP AMP
VREF	O	Reference voltage output, normally 1/2 V_{DD} .
X1	I	A crystal or ceramic resonator should be connected to this pin and X2. This pin may be driven from an external clock source.
X2	O	A crystal or ceramic resonator should be connected to this pin and X1.
DO	O	CAS detection output. Active high.
PDWN	I	A logic "1" on this pin puts the chip in power down mode. When a logic "0" is on this pin, the chip in power up mode. This is a Schmitt trigger input.
VDD	—	Positive power
VSS	—	Negative power, ground
AVDD	—	Analog positive power
AVSS	—	Analog negative power
NC	—	No connection

Absolute Maximum Ratings

Voltages are referenced to V_{SS} , except where noted.

Supply Voltage–0.5V to 6.0V All Input Voltages.....25mW

Operating Temperature Range0°C to 70°C Storage Temperature Range–40°C to 150°C

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

D.C. Characteristics

$T_a=25^{\circ}\text{C}$

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Unit
		V_{DD}	Conditions				
V_{DD}	Supply Voltage	—	—	3.5	—	5.5	V
AV_{DD}	Supply Voltage	3.5V~5.5V	$AV_{DD}=V_{DD}$	V_{DD}	—	V_{DD}	V
I_{DD}	Operating Current	5V	PDWN=0, 3.58MHz on	—	2.5	3	mA
I_{STBY}	Standby Current	5V	PDWN=1, 3.58MHz off	—	1	2	μA
V_{IL}	"Low" Input Voltage	5V	—	—	—	0.3	V_{DD}
V_{IH}	"High" Input Voltage	5V	—	0.7	—	—	V_{DD}
I_{OL}	Output Source Current (DO)	5V	$V_{OL}=0.1V_{DD}$	—	0.8	—	mA
I_{OH}	Output Sink Current (DO)	5V	$V_{OH}=0.9V_{DD}$	—	–0.3	—	mA
f_{SYS}	System Clock	—	—	—	3.58	—	MHz

A.C. Characteristics

Ta=25°C

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Unit
		V _{DD}	Conditions				
	Low Tone Frequency	5V	—	—	2130	—	Hz
	High Tone Frequency	5V	—	—	2750	—	Hz
	Frequency Deviation Acceptance	5V	Range within which, tones are accepted	—	—	±0.7	%
	Frequency Deviation Reject	5V	Range outside of which, tones are rejected	±2.5	—	—	%
	Acceptable Signal Level per Tone	5V	No speech	-33	—	0	dBm
	Reject Signal Level per Tone	5V	No speech	—	-36	—	dBm
	Twist Accept Limit	5V	—	—	±7	—	dB
	Signal to Noise Ratio	5V	—	—	-19	—	dB
t _{DET}	Present Detect Time	5V	—	—	55	—	ms

Clock (System Frequency)

Ta=25°C

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Unit
		V _{DD}	Conditions				
f _C	Clock Input Frequency	5V	—	3.5759	3.5795	3.5831	MHz
t _{LHCL}	Clock Input Rising Time	5V	—	—	—	110	ns
t _{HLCL}	Clock Input Falling Time	5V	—	—	—	110	ns
DC _{CL}	Clock Input Duty Cycle	5V	—	40	50	60	%

Functional Description

Power Down Mode

If PDWN pin= 1, the HT9033 is in reset status and the CAS detector is at power down mode, all function is disable. The oscillator circuit will be stop.

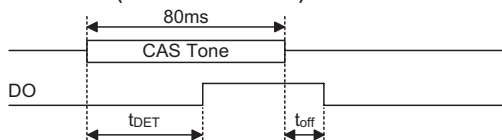
If PDWN pin= 0, CAS detector is activated.

CAS Detector Output Signal

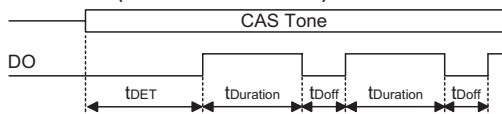
The output pin DO is CMOS structure. At the normal status (CAS tone is not presence), the DO pin output low. When the CAS tone is detected, the DO pin will output high level.

CAS Detector Timing

Condition 1 (Standard CAS Tone):



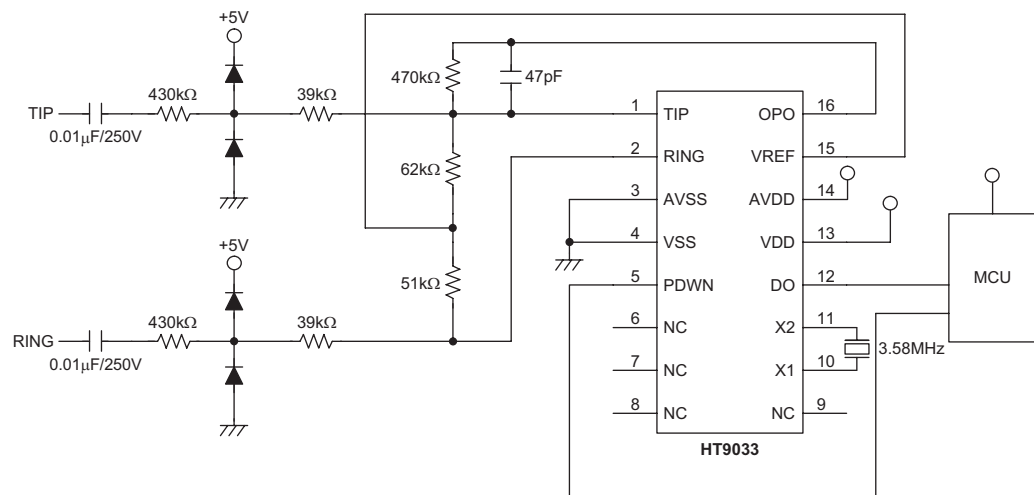
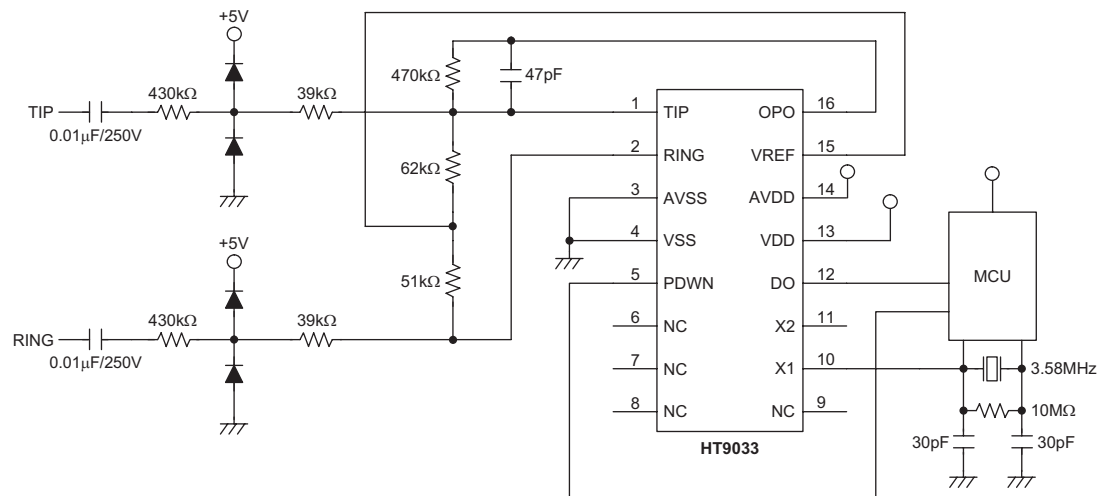
Condition 2 (Continuous CAS Tone):

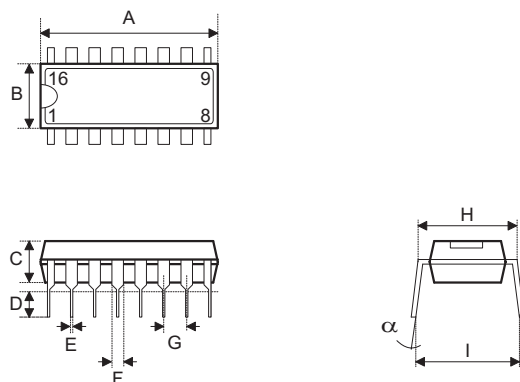


Parameter	Note	Min.	Typ.	Max.	Unit
t _{DET}	CAS Detecting Time	—	55	—	ms
t _{off}	DO Off Time	—	8	—	ms
t _{Duration}	DO Duration Time	—	145	—	ms
t _{Doff}	DO Duration Off Time	—	48	—	ms

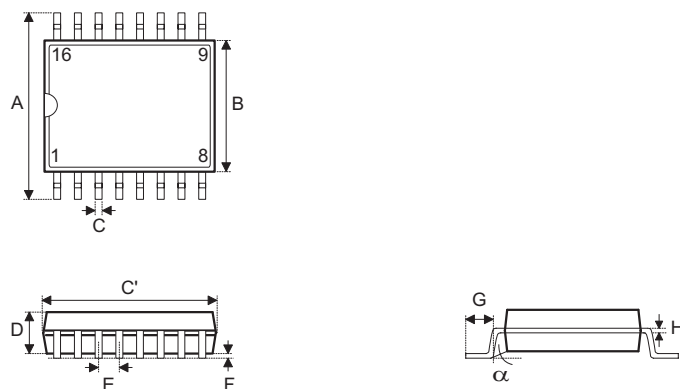
If the CAS tone is presence more than t_{DET} time, the CAS tone will be detected, then the DO will output high.

Application Circuits

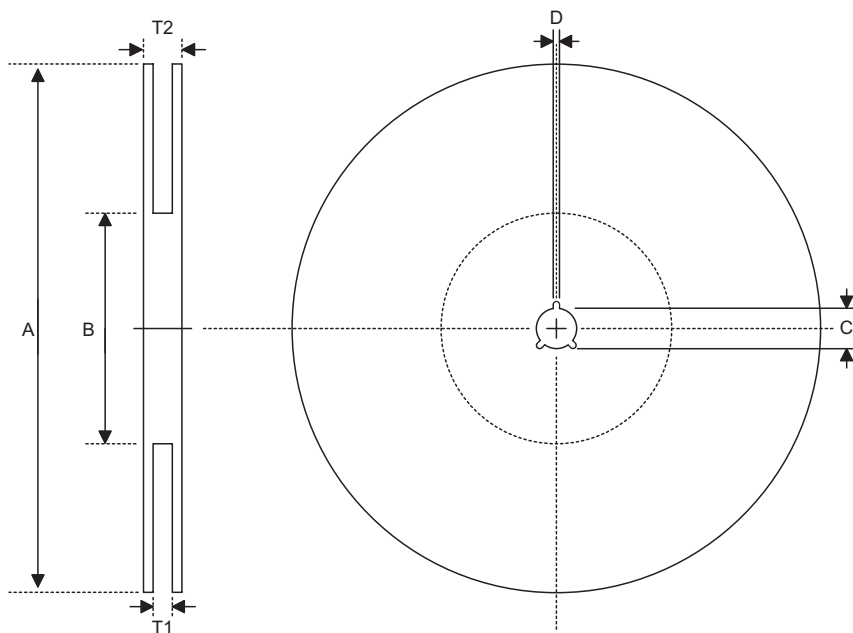


Package Information
16-pin DIP (300mil) Outline Dimensions


Symbol	Dimensions in mil		
	Min.	Nom.	Max.
A	745	—	775
B	240	—	260
C	125	—	135
D	125	—	145
E	16	—	20
F	50	—	70
G	—	100	—
H	295	—	315
I	335	—	375
α	0°	—	15°

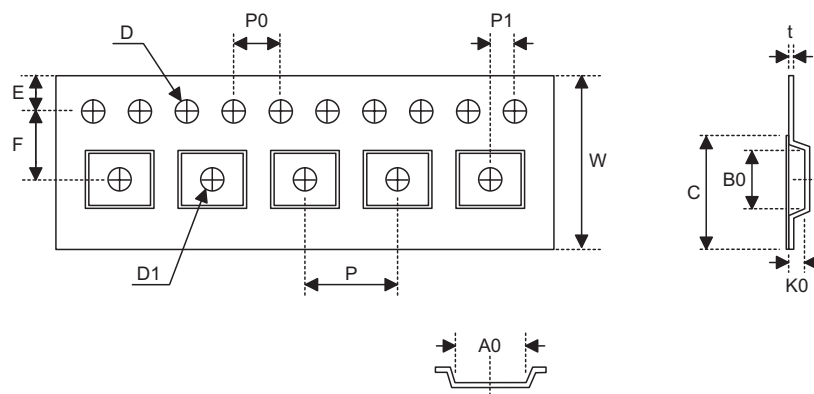
16-pin SOP (300mil) Outline Dimensions


Symbol	Dimensions in mil		
	Min.	Nom.	Max.
A	394	—	419
B	290	—	300
C	14	—	20
C'	390	—	413
D	92	—	104
E	—	50	—
F	4	—	—
G	32	—	38
H	4	—	12
α	0°	—	10°

Product Tape and Reel Specifications
Reel Dimensions


SOP 16W (300mil)

Symbol	Description	Dimensions in mm
A	Reel Outer Diameter	330±1
B	Reel Inner Diameter	62±1.5
C	Spindle Hole Diameter	13±0.5 -0.2
D	Key Slit Width	2±0.5
T1	Space Between Flange	16.8±0.3 -0.2
T2	Reel Thickness	22.2±0.2

Carrier Tape Dimensions


SOP 16W (300mil)

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	16±0.2
P	Cavity Pitch	12±0.1
E	Perforation Position	1.75±0.1
F	Cavity to Perforation (Width Direction)	7.5±0.1
D	Perforation Diameter	1.5+0.1
D1	Cavity Hole Diameter	1.5+0.25
P0	Perforation Pitch	4±0.1
P1	Cavity to Perforation (Length Direction)	2±0.1
A0	Cavity Length	10.9±0.1
B0	Cavity Width	10.8±0.1
K0	Cavity Depth	3±0.1
t	Carrier Tape Thickness	0.3±0.05
C	Cover Tape Width	13.3

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