

HT1015-1 1.5V Low Power LDO

Features

- · Low power consumption
- · Low voltage drop
- · Low temperature coefficient

- Wide operating voltage (12V max.)
- TO-92, SOT-89 and SOT-25 package

Applications

- Battery-powered equipment
- · Communication equipment

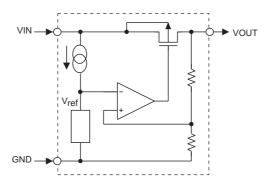
· Audio/Video equipment

General Description

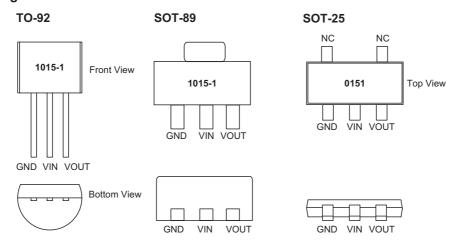
The HT1015-1 is a three-terminal low power voltage regulator implemented in CMOS technology. It is available with a fixed output voltage at 1.5V. CMOS technology ensures low voltage drop and low quiescent current.

Although designed primarily as a fixed voltage regulator, this device can be used with external components to obtain variable voltages and currents.

Block Diagram



Pin Assignment



Note: For lead free devices, TO-92 package will add a "#" mark at the end of the date code, whereas SOT-89 and SOT-25 packages will add a "#" mark at the end of the marking.



Absolute Maximum Ratings

Supply VoltageV _{SS} -0.3V to V _{SS} +13V	Storage Temperature50°C to 125°C
Power Consumption (*1)200mW	Operating Temperature40°C to 85°C
Power Consumption (*2)150mW	

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

Electrical Characteristics

Ta=25°C

Symbol	Symbol Parameter		Test Conditions		Tun	Max.	Unit
Syllibol	Parameter	V _{IN}	Conditions	Min.	Тур.	wax.	Unit
V _{OUT}	Output Voltage Tolerance	3.5V	I _{OUT} =0.5mA	1.455	1.5	1.545	V
I _{OUT}	Output Current	3.5V	_	7.0	18	_	mA
ΔV _{OUT}	Load Regulation	3.5V	1mA≤l _{OUT} ≤7mA	_	15	_	mV
V _{DIF}	Voltage Drop	_	I _{OUT} =0.5mA	_	250	_	mV
I _{SS}	Current Consumption	3.5V	No load	_	2.2	5.0	μΑ
$\frac{\Delta V_{\text{OUT}}}{\Delta V_{\text{IN}} x V_{\text{OUT}}}$	Line Regulation	_	2.5V≤V _{IN} ≤12V I _{OUT} =0.5mA	_	0.1	_	%/V
V _{IN}	Input Voltage	_	_	_	_	12	V
$\frac{\Delta V_{\text{OUT}}}{\Delta \text{Ta}}$	Temperature Coefficient	3.5V	I _{OUT} =0.5mA -40°C~85°C	_	-0.75	_	mV/°C

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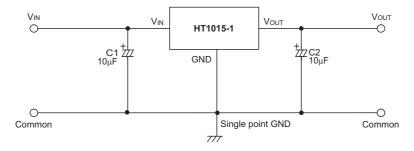
^{*1:} applied to SOT-89 and TO-92

^{*2:} applied to SOT-25

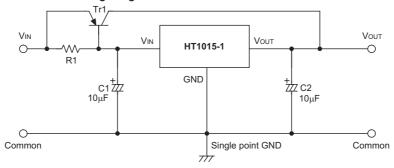


Application Circuits

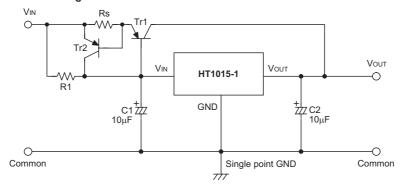
Basic Circuit



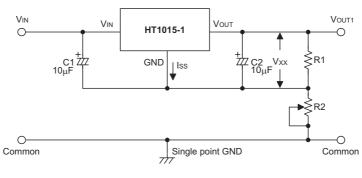
High Output Current Positive Voltage Regulator



Short-Circuit Protection Using External Transistors

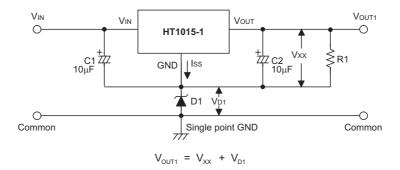


Increased Output Voltage Circuits

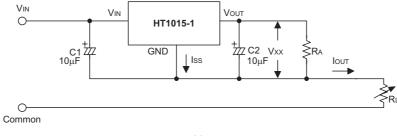


$$V_{OUT1} = V_{XX} (1 + \frac{R2}{R1}) + I_{SS} R2$$



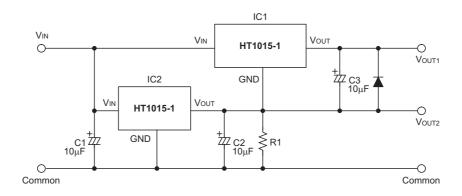


Constant Current Regulator



$$I_{OUT} = \frac{V_{XX}}{R_A} + I_{SS}$$

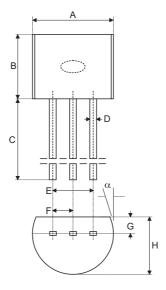
Dual Supply





Package Information

3-pin TO-92 Outline Dimensions

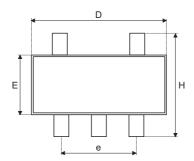


Complete	Dimensions in mil			
Symbol	Min.	Nom.	Max.	
Α	170	_	200	
В	170	_	200	
С	500	_	_	
D	11	_	20	
E	90	_	110	
F	45	_	55	
G	45	_	65	
Н	130	_	160	
I	8		18	
α	4°	_	6°	

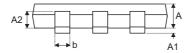
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5-pin SOT-25 Outline Dimensions



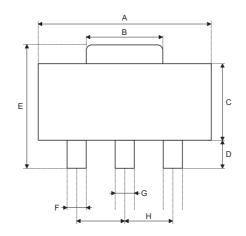


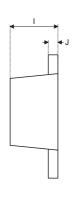


Comple at	Dimensions in mm		
Symbol	Min.	Nom.	Max.
Α	1	_	1.3
A1	_	_	0.1
A2	0.7	_	0.9
b	0.35	_	0.5
С	0.1	_	0.25
D	2.7	_	3.1
E	1.4	_	1.8
е	_	1.9	_
Н	2.6	_	3
L	0.37	_	_
θ	1°	_	9°



3-pin SOT-89 Outline Dimensions



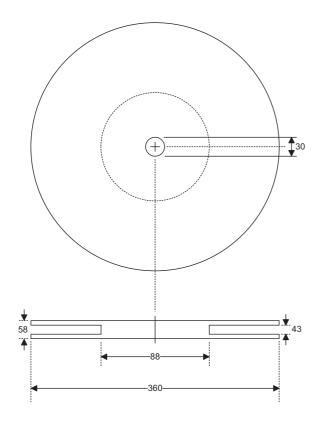


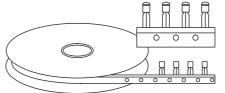
Cumbal		Dimensions in mil		
Symbol	Min.	Nom.	Max.	
А	173	_	181	
В	64	_	72	
С	90	_	102	
D	35	_	47	
E	155	_	167	
F	14	_	19	
G	17	_	22	
Н	_	59	_	
I	55	_	63	
J	14	_	17	



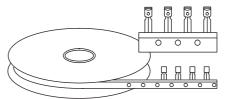
Product Tape and Reel Specifications

TO-92 Reel Dimensions (Unit: mm)





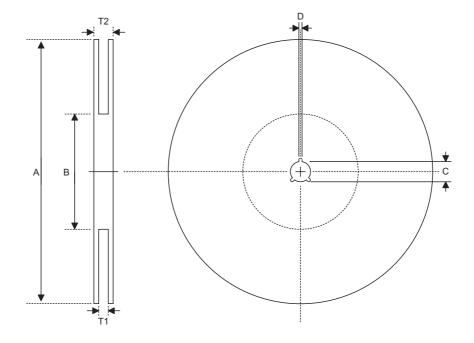
Package Up, Flat Side Up



Package Up, Flat Side Down



Reel Dimensions



SOT-89

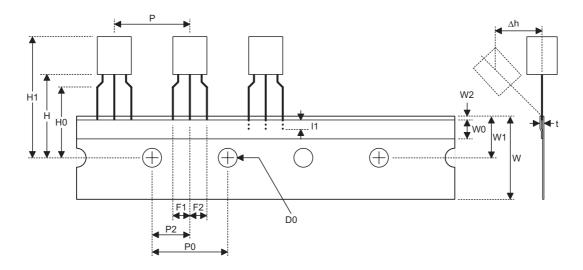
Symbol	Description	Dimensions in mm
Α	Reel Outer Diameter	180±1.0
В	Reel Inner Diameter	62±1.5
С	Spindle Hole Diameter	12.75+0.15
D	Key Slit Width	1.9±0.15
T1	Space Between Flange	12.4+0.2
T2	Reel Thickness	17–0.4

SOT-25

Symbol	Description	Dimensions in mm
А	Reel Outer Diameter	178±1
В	Reel Inner Diameter	62±1
С	Spindle Hole Diameter	13±0.2
D	Key Slit Width	2.5±0.25
T1	Space Between Flange	8.4+1.5
T2	Reel Thickness	11.4+1.5



Carrier Tape Dimensions



TO-92

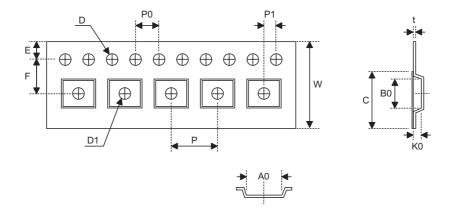
Symbol	Description	Dimensions in mm
I1	Taped Lead Length	(2.5)
Р	Component Pitch	12.7±1.0
P0	Perforation Pitch	12.7±0.3
P2	Component to Perforation (Length Direction)	6.35±0.4
F1	Lead Spread	2.5+0.4 -0.1
F2	Lead Spread	2.5+0.4 -0.1
Δh	Component Alignment	0±0.1
W	Carrier Tape Width	18.0+1.0 -0.5
W0	Hold-down Tape Width	6.0±0.5
W1	Perforation Position	9.0±0.5
W2	Hold-down Tape Position	(0.5)
H0	Lead Clinch Height	16.0±0.5
H1	Component Height	Less than 24.7
D0	Perforation Diameter	4.0±0.2
t	Taped Lead Thickness	0.7±0.2
Н	Component Base Height	19.0±0.5

Note: Thickness less than 0.38±0.05mm~0.5mm

P0 Accumulated pitch tolerance: ±1mm/20pitches.

() Bracketed figures are for consultation only





SOT-89

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	12.0+0.3 -0.1
Р	Cavity Pitch	8.0±0.1
E	Perforation Position	1.75±0.1
F	Cavity to Perforation (Width Direction)	5.5±0.05
D	Perforation Diameter	1.5+0.1
D1	Cavity Hole Diameter	1.5+0.1
P0	Perforation Pitch	4.0±0.1
P1	Cavity to Perforation (Length Direction)	2.0±0.10
A0	Cavity Length	4.8±0.1
В0	Cavity Width	4.5±0.1
K0	Cavity Depth	1.8±0.1
t	Carrier Tape Thickness	0.30±0.013
С	Cover Tape Width	9.3

SOT-25

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	8±0.3
Р	Cavity Pitch	4
Е	Perforation Position	1.75
F	Cavity to Perforation (Width Direction)	3.5±0.05
D	Perforation Diameter	1.5+0.1
D1	Cavity Hole Diameter	1.5+0.1
P0	Perforation Pitch	4
P1	Cavity to Perforation (Length Direction)	2
A0	Cavity Length	3.15
В0	Cavity Width	3.2
K0	Cavity Depth	1.4
t	Carrier Tape Thickness	0.2±0.03
С	Cover Tape Width	5.3

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