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RECORDS OF REVISION

Date	Ver.	Description	Page	Design by
2008/1/16	0	The sample (NO ROHS PRODUCTION)has changed the LCD ,which was based on the Powertip's MASS PRODUCTION : PC1601LRS-EWA-B		林海艷
2008/2/18	0	MASS PRODUCTION		林海艷

Total: 24 Page



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Note : For detailed information please refer to IC data sheet : <u>ST7066U-0A</u>



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*1 Characters
LCD Type	STN Gray Positive Transflective Extended Temp.
Driver Condition	LCD Module : 1/16 Duty , 1/4 Bias
Viewing Direction	6 O'clock
Backlight	LED YG B/L
Weight	_
Interface	_
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web side :
	http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	80.0(L) * 36.0(w) * 14.0(H)(Max)	mm
Viewing Area	65.0(L) * 16.0(w)	mm
Active Area	59.62(L) * 6.56(w)	mm
Dot Size	0.55(L) * 0.75(w)	mm
Dot Pitch	0.63(L) * 0.83(w)	mm

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V _{DD}	_	-0.3	7.0	V
LCD Driver Supply Voltage	V _{LCD}	_	Vdd-10.0	VDD+0.3	V
Input Voltage	$V_{\rm IN}$	_	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	_	-20	70	°C
Storage Temperature	T _{ST}	_	-30	80	°C
Storage Humidity	H _D	Ta<60 °C	-	90	%RH



1.4 DC Electrical Characteristics

	V , $V_{SS} =$	0V , Ta =	= 25°C			
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V _{DD}	_	4.5	5.0	5.5	V
"H" Input Voltage	V _{IH}	_	0.7 Vdd	-	Vdd	V
"L" Input Voltage	V _{IL}	_	-0.3	-	0.6	V
"H" Output Voltage	V _{OH}	IOH=-0.1mA	3.9	-	Vdd	V
"L" Output Voltage	V _{OL}	IOL=0.1mA	-	-	0.4	V
	T	VDD=5.0V;Vop=4.5V; Pattern= Full display	-	1.1	-	
Supply Current	I _{DD}	V _{DD} =5.0V; Vop=4.5V; Pattern= Perpendicular line*1			3.0	mA
		-20°C	4.4	4.6	4.8	
LCM Driver Voltage	V _{OP} *2	25°C	4.3	4.5	4.7	V
		70°C	4.1	4.3	4.5	

NOTE: *1 The Maximum current display;

*2 The VOP test point is VDD-VO.

1.5 Optical Characteristics

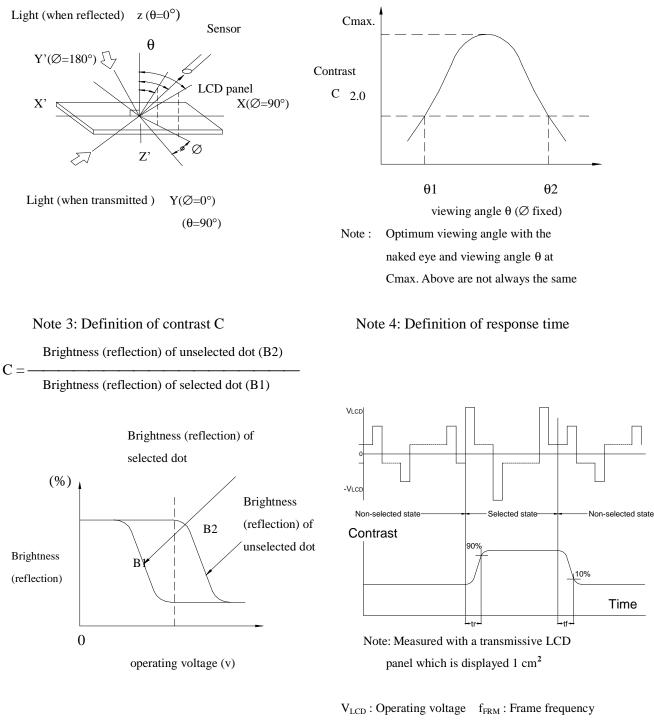
LCD Panel : 1/16 Duty , 1/4 Bias , V_{LCD} =4.2V , Ta = 25 $^\circ\!\mathrm{C}$

Item	Symbol	Conditions	Min.	Тур.	Max.	Reference
View Angle	θ	C \geq 2.0, Ø = 0°	0°	-	40°	Notes 1 & 2
Contrast Ratio	С	$\theta = 5^{\circ}, \emptyset = 0^{\circ}$	5	7	-	Note 3
Response Time(rise)	tr	$\theta = 5^{\circ}, \emptyset = 0^{\circ}$	-	150 ms	-	Note 4
Response Time(fall)	tf	$\theta = 5^{\circ}, \emptyset = 0^{\circ}$	-	300 ms	-	Note 4



Note 1: Definition of angles θ and \varnothing

Note 2: Definition of viewing angles $\theta 1$ and $\theta 2$



 t_r : Response time (rise) t_f : Response time (fall)



1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	300	mA
Reverse Voltage	VR	Ta =25°C	-	8	V
Power Dissipation	РО	Ta =25°C	-	1.38	W
Operating Temperature	T _{OP}	-	-20	70	°C
Storage Temperature	T _{ST}	-	-30	80	°C
Solder Temp. for 3 Second	-	-	-	330	°C

Electrical / Optical Characteristics

Ta =25℃

					1a –	1 5 ()	
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Forward Voltage	VF	IF= 120 mA	-	4.2	4.6	V	
Reverse Current	IR	VR= 8 V	-	-	0.2	mA	
Average Brightness (with LCD) *1	IV	IF=120 mA	30	45	-	cd/m ²	
Wavelength	λp	IF=120 mA	569	-	576	nm	
Uniformity *2	∆B	IF =120 mA	70	-	-	%	
Color	Yellow-green						

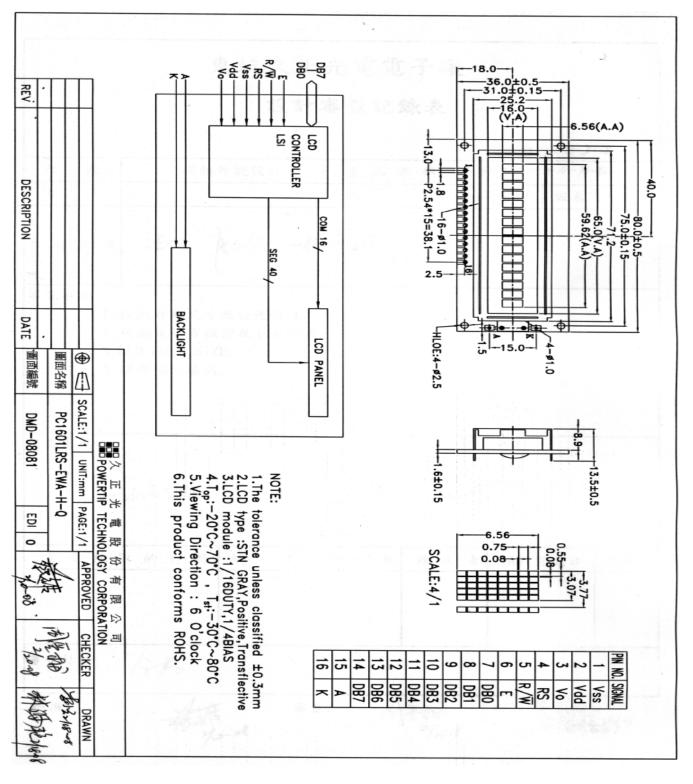
*1 This vaule will be changed while mass production

*2 $\Delta B=B(min)/B(max)$



2. MODULE STRUCTURE

2.1 Counter Drawing



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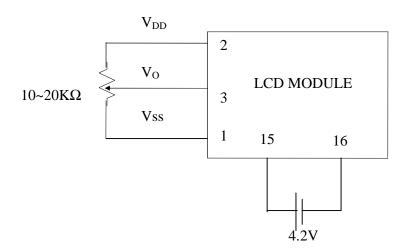
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2.2 Interface Pin Description

Pin No.	Symbol	Signal Description						
1	Vss	Signal ground (GND)						
2	VDD	Power Supply (VDD> VSS)						
3	Vo	Operating voltage (LCD Driver)						
		Register Selection input						
4		High = Data register						
4	RS	Low = Instruction register (for write)						
		Busy flag address counter (for read)						
5		Read/Write signal input is used to select the read/write						
5	R/W	mode. High = Read mode, Low = Write mode						
6	Е	Start enable signal to read or write the data						
		Four low order bi-directional three-state data bus lines. Use						
7~10	DB0 ~ DB3	for data transfer between the MPU and the LCD module.						
		These four are not used during 4-bit operation.						
		Four high order bi-directional three-state data bus lines.						
11~14	DB4 ~ DB7	Used for data transfer between the MPU and the LCD						
11~14	$DB4 \sim DB7$	module.						
		DB7 can be used as a busy flag.						
15	А	Power supply LED backlight (+)						
16	К	Power supply LED backlight (-)						

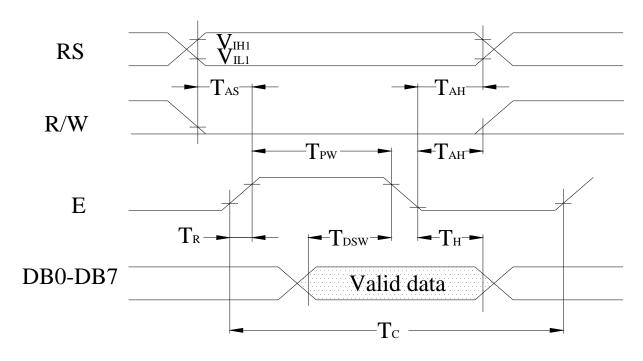
Contrast Adjust



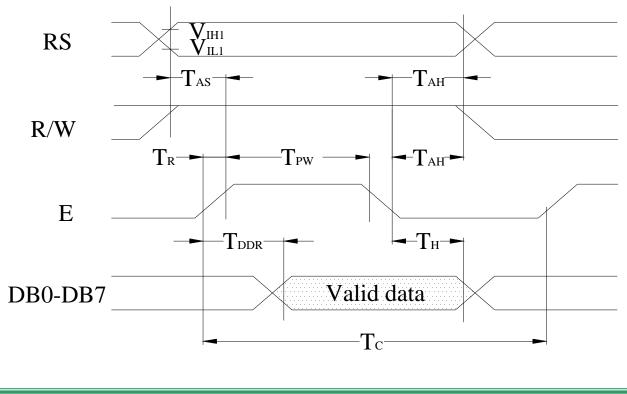


2.3 Timing Characteristics

• Writing data from MPU to ST7066U



• Reading data from ST7066U to MPU





• Write Mode (Writing data from MPU to ST7066U)

 $(VDD = +5V, Ta = 25^{\circ}C)$

Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
		-	-	199.	111471	Ome
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_{DSW}	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
T_{H}	Data Hold Time	Pins:DB0~DB7	10	_	-	ns

• Read Mode (Reading data from ST7066U to MPU)

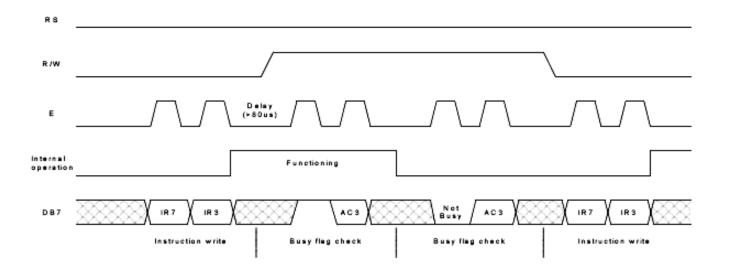
 $(VDD = +5V, Ta = 25^{\circ}C)$

						,,1a <u>2</u> 0 C
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

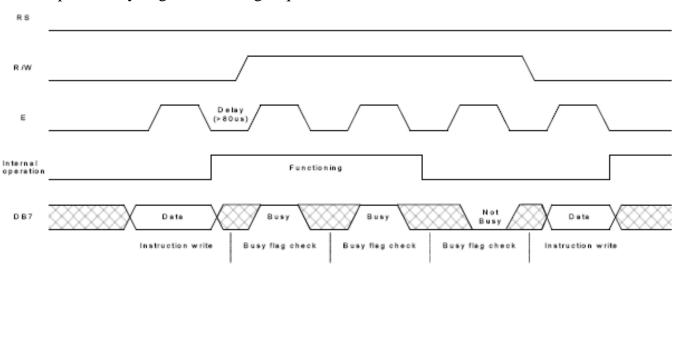


For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer.

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used .



Example of busy flag check timing sequence



2.4 Display Command

					Instru	iction	Code					Description
Instructions	RS	R/W	DB 7	DB 6	DB 5	DB 4	DB 3	DB 2	DB 1	DB 0	Description	Time (270KHz)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	×	Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	37us
Display ON/OFF	0	0	0	0	0	0	1	D	С	В	D=1 : entire display on C=1 : cursor on B=1 : cursor position on	37µs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	×	×	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	37µs
Function Set	0	0	0	0	1	DL	N	F	×	×	DL: interface data is 8/4 bits NL: number of line is 2/1 F: font size is 5×11/5×8	37µs
Set CGRAM Address	0	0	0	1	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set CGRAM address in address counter.	37µs
Set DDRAM Address	0	0	1	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set DDRAM address in address counter.	37µs



Read Busy Flag and Address	0	1	BF	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0µs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF. Refer to Instruction Table for the list of each instruction execution time .

POWERTIP

2.5 Character Pattern

CHARACTER PATTERN(SO/HO/EA,WA)

Land Illin	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
****0000	66 RAM (1)			Ø	Ð	P	••	₽₽					-57	₩,	œ	
*****	(2)		ļ	1	Ē	Q	.=	-::4			3	- <u>1</u> -1	ij.	ć.,		C
xxxx0010	(3)		••	2	B	R	b	ŀ			L.	-1	ij	;×*	⊫	e
****0011	(4)		#	3	C	S	़					r'j	Ŧ	Æ	€.	~
****0100	(6)		\$	ı::].	D	Т	d	t.		-	•.	T	ŀ	† ?	4	57
****0101	(6)		2	5	E	U	e	u				7	<u>-</u>	.1.	œ	ü
*******	(7)		8:	6	F	Ų	£	Q		:	Ņ	<u>;</u> †;)		=	ρ	Σ
****0111	(8)		7	7	Gi	IJ	g	IJ			7	:: ::	37		9	J
****1000	00		<	3		\times	h	\times			ť	7	:‡:	Ņ	.,r-	×
KXXX 1001	(9)		>	9	T	Ŷ	i	י يا			-:	÷Ţ	ļ	ıĿ	1	Ŀ
****1010	(3)		44	#	J	Z	j.	2			:E:		ı `ı	Ŀ	j.	=p:
****1011	(4)			;	K	Ľ	ŀ∶	<			:4	ψ.	<u> </u>		×	35
0001100	(5)		7	<	L	₿¢Ë	1	I		•	! ??	<u>.</u> ,ı		ņ	¢:-	1::::
	(6)				ŀΊ		m	3			: 1.	2	^,		ŧ	÷
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0001111	(0)			?	D		$^{\circ}$	÷			ц	9	2		ö	

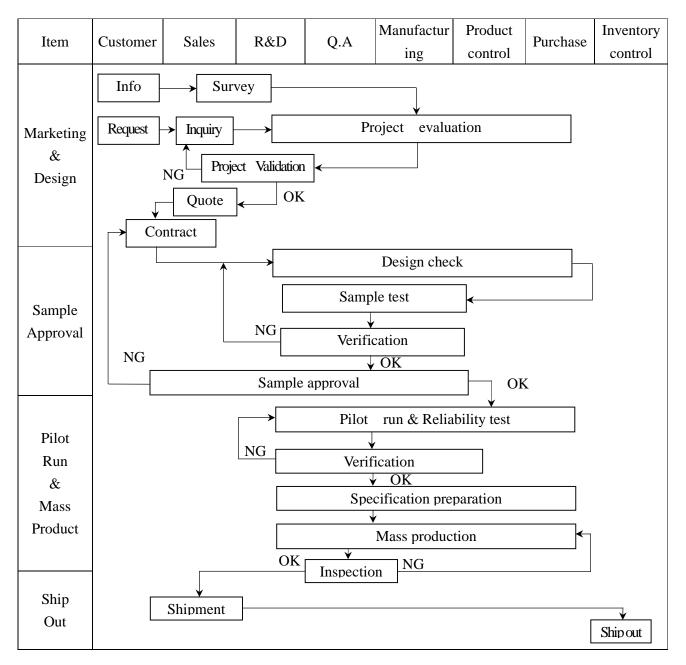
2.6 JUMPER(Setting different use)

- 2.6.1 SHORT : J1/J3/J5
- 2.6.2 **OPEN : The other unnoted jumpers**

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3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



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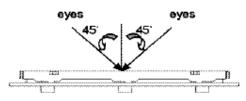
Item	Customer	Sales	R&D	Q.A	Manufactu ring	Product control	Purchase	Inventory control
Sales Service	Info	→ Claim sis report		Trackin	Failure an Corrective	-		
Q.A Activity	 ISO 9001 Equipme Standardi 		n		posal Activities			

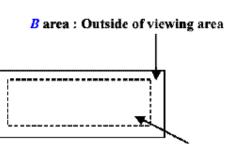


3.2 Inspection Specification

◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- Defect Level : Major Defect AQL 0.4; Minor Defect AQL 1.5.
- OUT Going Defect Level : Sampling .
- ◆Manner of appearance test :
 - (1). The test be under $40W \times 2$ fluorescent light ' and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (3). Definition of area . (Fig. 2)





A area : viewing area

◆ Specification:

NO	Item	Criterion	level				
		1.1 The part number is inconsistent with work order of Production.	Major				
01	Product condition	1.2 Mixed production types.					
		1.3 Assembled in inverse direction.	Major				
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major				
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major				
		4.1 Missing line character dot and icon.	Major				
		4.2 No function or no display.	Major				
04	Electrical Testing	4.3 Output data is error.					
		4.4 LCD viewing angle defect.					
		4.5 Current consumption exceeds product specifications.	Major				
05	Black or white dot < scratch < contamination Round type	 5.1 Round type: 5.1.1 display only : White and black spots on display ≤ 0.30mm, no more than Four white or black spots present. Densely spaced : NO more than two spots or lines within 3mm 	Minor				

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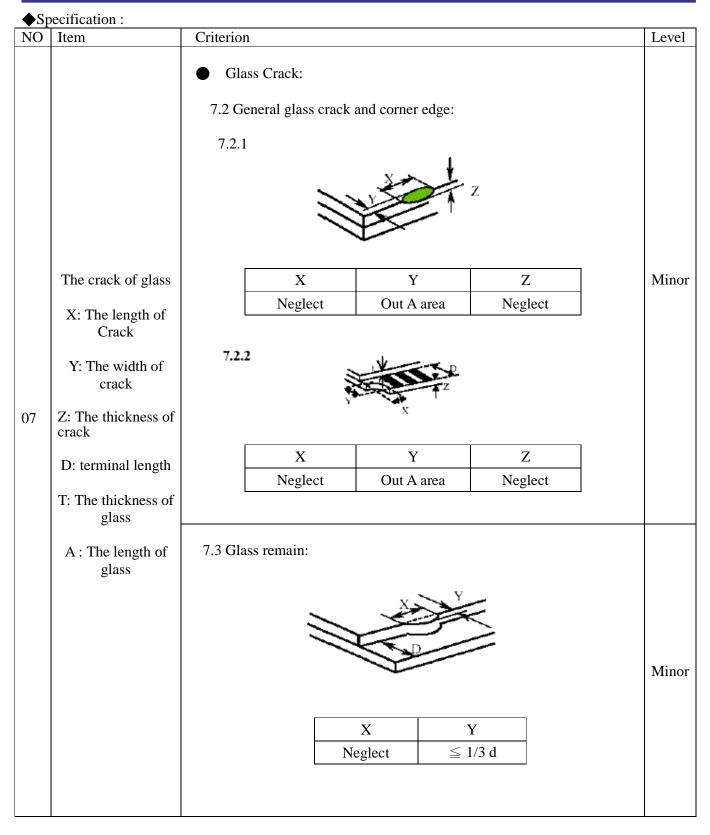


	ecification :	Criterier							11	
NO 05	Item	Criterion							level	
05	Black or white dot scratch contamination Round type		5.1.2 Nom-display :							
		0								
			$\begin{array}{ c c c c c c c c c c c c c c c c c c c$							
	<u> </u>	0.	$\frac{200000}{\text{Total}}$	111111		4				
	.		Total							
	$\Phi = (x+y)/2$	5.1.3 Line t							Minor	
	÷ (,), =	Dimens	Dimension (diameter : Φ)Acceptance (Q'ty)							
		Length	width		A area		B area			
	Т		$w \leq 0.03 mm$		Accept no de	ense	Don't co			
		L≦3.0mm	0.03 mm $< \Phi \le 0$				Don't co			
	·). →	L≦2.5mm	0.05 mm $< \Phi \leq 0$		4		Don't co	unt		
	L		w>0.075m	m	As	round	d type			
				1		(0)	<u> </u>			
				eter : Φ) Acceptance		B area				
		Dimension	(diameter : Φ)				D alea			
		$\Phi \leq$	≦0.20mm	Accept no dense			Don't count			
06	Polarizer	0.20mm <	$< \Phi \leq 0.50$ mm	3			Don't count		Minor	
00	Bubble	0.50mm <	$< \Phi \leq 1.00$ mm	2			Don't count			
		Φ >	>1.00mm		0		Don't count			
		Tota	l quantity	4			Don't coun	t		
		Glass	Crack:							
		7.1 Crac	ck on the circuit of	felectroc	le terminal :					
	The crack of		\sim		c					
07	glass									
07										
			Y Y							
			X		Y		Ζ			
		Fro		2	$\frac{1}{Y \le 1/2 D}$,	$Z \le t$			
				a			ב≟ו			
		Bac	СК		Neglect					

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◆Specification :

NO	ecification : Item	Criterion	Level					
07	The crack of glass X: The length of Crack Y: The width of crack Z: The thickness of crack D: terminal length T: The thickness of glass	7.4 Corner crack and medial crack: $ \begin{array}{c} $	Minor					
	A : The length of glass	XYZ $\leq 1/5a$ Crack can't enter viewing area $\leq 1/2t$ $\leq 1/5a$ Crack can't exceed the half of uridth of SD pridth of SD $1/2t < Z \leq 2t$						
		width of SP width of SP 8.1 Backlight can't work normally.	Major					
	Paaldight	8.2 Backlight doesn't light or color is wrong.	Major					
08	Backlight elements	8.3 Illumination source flickers when lit.						
		9.1 pin type must match type in specification sheet						
		9.2 No short circuits in components on PCB or FPC						
	General	9.3Product packaging must the same as specified on						
09	appearance	packaging specification sheet.	Major					
		9.4 The folding and peeled off in polarizer are not acceptable						
		9.5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤ 1.5 mm						



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION								
1	High Temperature Storage Test	Keep in 80	$\pm 2^{\circ}$ C 96 hrs							
		Surrounding	temperature, then stora	ge at normal conditior	n 4hrs					
2	Low Temperature Storage Test	Keep in $-30 \pm 2^{\circ}$ C 96 hrs								
		Surrounding	temperature, then stora	ge at normal conditior	n 4hrs					
		Keep in +60°	°C/90%RH duration for	96 hrs						
			temperature, then stora	ge at normal conditior	n 4hrs					
2	High Humidity Storage		he polarizer)Or	0.61						
3	High Humidity Storage	-	°C/90%RH duration for		41					
			temperature, then stora							
		Air Discharg	ge:	Contact Discharge:						
		Apply 6 KV	with 5 times	Apply 250V with 5	times					
			r each polarity +/-	discharge for each	polarity +/-					
		1. Temperature ambient: 15° C $\sim 35^{\circ}$ C								
		2. Humidity relative: $30\% \sim 60\%$								
4	ESD Test	3. Energy Storage Capacitance(Cs+Cd):150pF±10%								
		4. Discharge Resistance(Rd):330 $\Omega \pm 10\%$								
		5. Discharge, mode of operation:								
		Single Discharge (time between successive discharges at least 1 s)								
		(Tolerance If the output voltage indication: $\pm 5\%$)								
		$-20^{\circ}\text{C} \rightarrow 25^{\circ}\text{C} \rightarrow 70^{\circ}\text{C} \rightarrow 25^{\circ}\text{C}$								
5	Temperature Cycling Test	(30mins) (5mins) (30mins) (5mins)								
5		10 Cycle								
		Surrounding temperature, then storage at normal condition 4hrs								
		1. Sine wave $10 \sim 55$ HZ frequency (1 min)								
6	Vibration Test (Packaged)	2. The amplitude of vibration :1.5 mm								
		3. Each direction (XYZ) duration for 2 Hrs								
		-	Packing Weight (Kg)	Drop Height (cm)						
			0 ~ 45.4	122						
			45.4 ~ 90.8	76						
7	Drop Test (Packaged)		90.8 ~ 454	61						
			Over 454	46						
		Dr	op direction : %3 come	1 edges /6 sides etch	1times					

POWERTIP

5. PRECAUTION RELATING PRODUCT HANDLING 5.1 SAFETY

5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.

5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

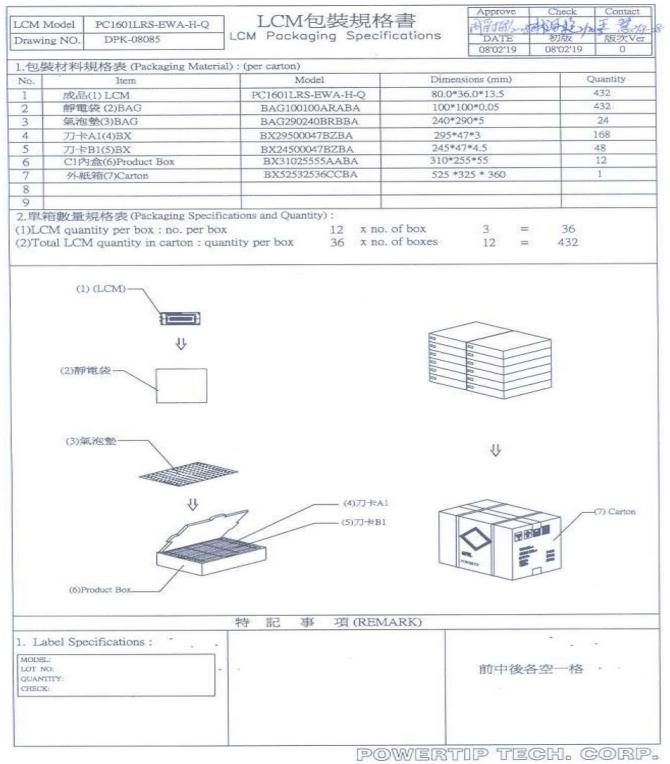
The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



6. PACKING Specification



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