

High(A + 16)	LSB	0000	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
LSB XXXXXXXX	01 LH CH		0	0	0	0	0	0	0	0	0	0	0	0	0	0
XXXXXXXX	0	!	1	A	Q	a	*				7	9	C			
XXXXXXXX	0	"	2	B	R	b	r				"	4	U	X	P	O
XXXXXXXX	0	#	3	C	S	c	s				#	5	E	e		
XXXXXXXX	0	\$	4	D	T	d	t				\$	6	F	f		
XXXXXXXX	0	%	5	E	L	e	l				%	7	G	g		
XXXXXXXX	0	&	6	F	V	f	v				&	8	H	h		
XXXXXXXX	0	'	7	G	W	w					'	9	I	i		
XXXXXXXX	0	(	8	H	X	x					(	0	J	j		
XXXXXXXX	0	)	9	I	Y	y					)	1	K	k		
XXXXXXXX	0	*	0	J	Z	z					*	2	L	l		
XXXXXXXX	0	+	1	K	X	x					+	3	M	m		
XXXXXXXX	0	,	2	L	Y	y					,	4	N	n		
XXXXXXXX	0	=	3	M	Z	z					=	5	O	o		
XXXXXXXX	0	>	4	N	X	x					>	6	P	p		
XXXXXXXX	0	/	5	O	Y	y					/	7	Q	q		

■ Absolute Maximum Rating

Item	Symbol	Value	Unit
Power Supply Voltage	$V_{DD} - V_{SS}$	-0.3 ~ +7.0	V
Driver Supply Voltage	V_{LED}	$V_{DD} - 13.5 \sim V_{DD} + 0.3$	
Input Voltage	V_{IN}	-0.3 ~ $V_{DD} + 0.3$	
Operating temperature range	T_{OP}	0 ~ +50	°C
Storage temperature range	T_{ST}	-20 ~ +60	

■ Description of Terminals

Symbol	Input/ Output	External Connection	Function				
R S	Input	M P U	Register selection input				
			<table><tr><td>High</td><td>Data register (for read and write)</td></tr><tr><td>Low</td><td>Instruction register (for write) Busy flag, address counter (for read)</td></tr></table>	High	Data register (for read and write)	Low	Instruction register (for write) Busy flag, address counter (for read)
High	Data register (for read and write)						
Low	Instruction register (for write) Busy flag, address counter (for read)						
R / W	Input	M P U	R/W signal input is used to select the read/write mode				
			<table><tr><td>High</td><td>Read mode</td></tr><tr><td>Low</td><td>Write mode</td></tr></table>	High	Read mode	Low	Write mode
High	Read mode						
Low	Write mode						
E	Input	M P U	Start enable signal to read or write the data				
DB 4 DB 7	Input/ Output	M P U	Four high order bidirectional three-state data bus lines. Used for data transfer between the MPU and the LCD module. DB7 can be used as a busy flag.				
DB 0 DB 3	Input/ Output	M P U	Four low order bidirectional three-state data bus lines. Used for data transfer between the MPU and the LCD module. These four are not used during 4-bit operation.				
V d d V s s		Power Supply	V d d : + 5 V V s s : G N D				
V o		Power Supply	Contrast adjustment voltage				

ELECTRAL CHARACTERISTICS

Dc Characteristics (Vdd=+5V±10%, Vss=0V, Ta=25℃)

Parameter	Symbol	Condition	Applicable PIM	Min.	Typ.	Max.	Unit
H level input voltage(1)	Vih1	—	DB0 ~ DB7	2.2	—	Vdd	V
L level input voltage(1)	Vil1	—	RS,R/V,E	-0.3	—	0.6	V
H level input voltage(2)	Vih2	—	OSC1	Vdd-1.0	—	Vdd	V
L level input voltage(2)	Vil2	—		-0.2	—	1.0	V
H level output voltage(1)	Voh1	Ioh=-0.205mA	DB0 ~ DB7	2.4	—	—	V
L level output voltage(1)	Vol1	Iol=1.2mA		—	—	0.4	V
H level output voltage(2)	Voh2	Ioh=-40uA	XSC LP DO	0.9Vdd	—	—	V
L level output voltage(2)	Vol2	Iol=40uA		—	—	0.1Vdd	V
I/o leakage current	Iil	Vin=0 to Vdd		-1	—	1	uA
Pull-UP Mos. Current	-Ip	Vdd=5V		50	125	250	uA
Supply current	Iop	Rf oscillation from external clock Vdd=5v fosc=270kHz	Vdd	—	0.35	0.6	mA

Internal clock operation (Rf oscillation)

Oscillation frequency	fosc	Rf=91kΩ±2%	OSC1 OSC2	190	270	350	kHz
Oscillation frequency	fosc	Ceramic filter	OSC1 OSC2	245	250	255	kHz
LCD driving voltage	Vled	Vdd-Vs	V1~V5	3.0	—	11.0	V

Optical Characteristics

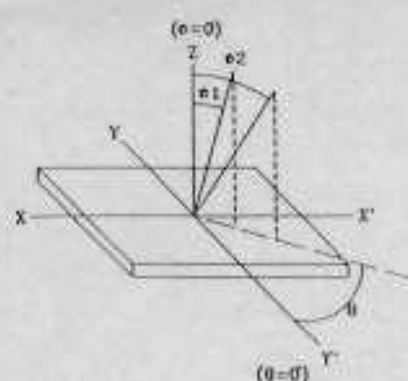
1. STN TYPE

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle	$\phi 2 - \phi 1$	$K=1.4$	40	—	—	deg.	*1, *2
Contrast Ratio	K	$\phi = 10^\circ$ $\theta = 0^\circ$	—	3	—	—	*3
Response Time(Rise)	tr	$\phi = 10^\circ$ $\theta = 0^\circ$	—	150	250	ms	*4
Response Time(Fall)	tf	$\phi = 10^\circ$ $\theta = 0^\circ$	—	200	300	ms	*4

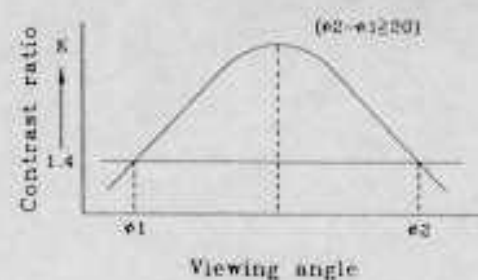
2. TN TYPE

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle	$\phi 2 - \phi 1$	$K=1.4$	60	—	—	deg.	*1, *2
Contrast Ratio	K	$\phi = 25^\circ$ $\theta = 0^\circ$	—	3	—	—	*3
Response Time(Rise)	tr	$\phi = 25^\circ$ $\theta = 0^\circ$	—	80	120	ms	*4
Response Time(Fall)	tf	$\phi = 25^\circ$ $\theta = 0^\circ$	—	60	90	ms	*4

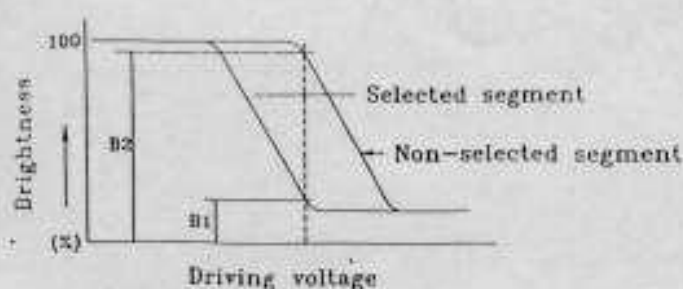
*1. Definition of ϕ AND θ



*2. Contrast vs viewing angle

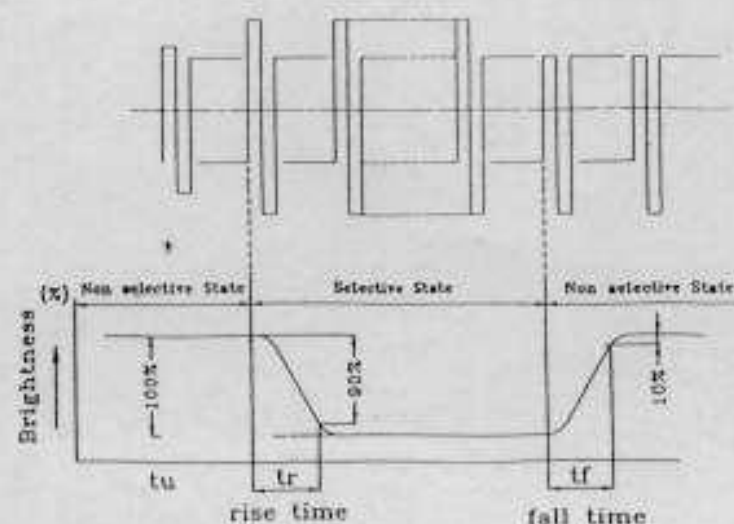


*3. definition of contrast ratio



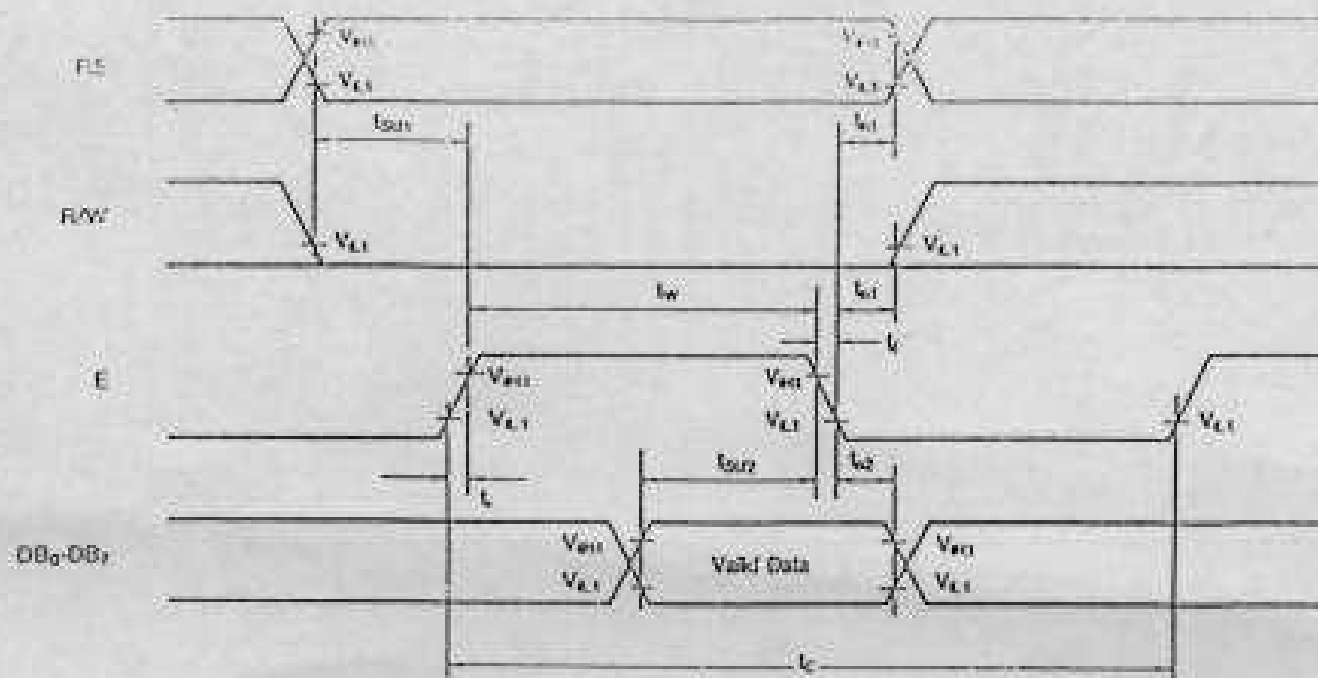
$$K = \frac{\text{Brightness of non-selected segment}(B2)}{\text{Brightness of selected segment}(B1)}$$

*4. Definition of optical response

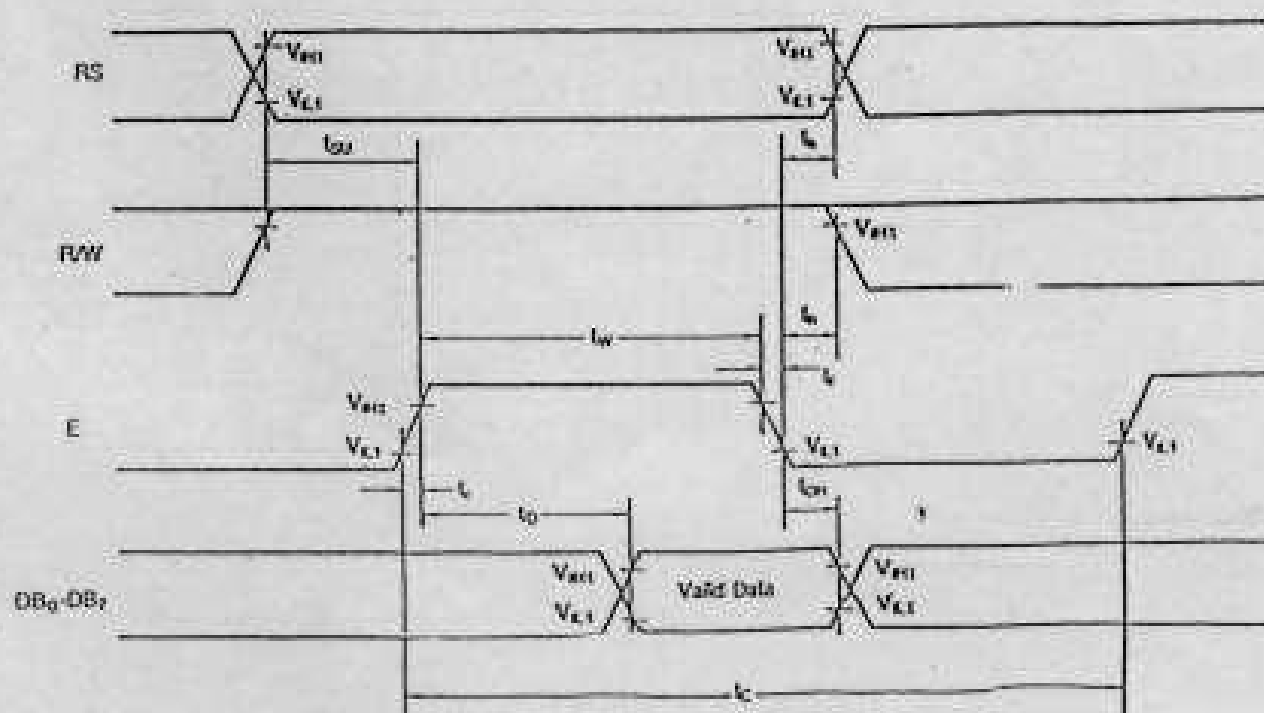


Timing Characteristics

(1) Write mode



(2) Read mode



■ DISPLAY COMMAND

Parameter	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	DESCRIPTION
CLEAR DISPLAY	0	0	0	0	0	0	0	0	0	1	
CURSOR HOME	0	0	0	0	0	0	0	0	1	=	
ENTRY MODE SET	0	0	0	0	0	0	0	1	I/O	SH	DB1=1:Increment DB1=0:Decrement DB0=1:The display is shifted DB0=0:The display is not shifted
DISPLAY ON/OFF	0	0	0	0	0	0	1	D	C	8	DB2=1:Display on DB2=0:Display off DB1=1:Cursor on DB1=0:Cursor off DB0=1:Brinking on DB0=0:Brinking off
CURSOR/DISPLAY SHIFT	0	0	0	0	0	1	S/C	R/L	=	=	DB3=1:Shifts display one character DB2=1:Right shift DB2=0:Left shift
FUNCTION SET	0	0	0	0	1	DL	H	F	=	=	DB4=1:8 bits DB4=0:4 bits DB3=1:2 lines display (1/16 duty) DB3=0:1 line display (DB2=1:5×10 dots,1/11 duty DB2=0:5×7 dots,1/8 duty)
SET CGRAM ADDRESS	0	0	0	1	ACG						The address length that can be set is 64 addresses
SET DDRAW ADDRESS	0	0	1	ADD						The address length that can be set is 80 addresses	
READ BUSY FLAG/ ADDRESS COUNTER	0	1	B/F	AC						DB7=1:Busy(instruction not accepted) DB7=0:Ready (instruction accepted)	
WRITE DATA	1	0	Write Data								
READ DATA	1	1	Read Data								

NOTE: = Don't care.