

POWERTIP TECH. CORP.

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

Specification For Approval

Customer		:		_
Model Type		: <u>L</u>	CD MODULE	
Sample Code	:	:		<u> </u>
Mass Produc	tion Code	: <u>PG1</u>	2864WRM-K	NN-I
Edition		: _0_		
Customer Sign	Sales Sign	Checked By (QA)	Approved By	Prepared By
			MERCH STOP-ON	刺肠蓬

Revision Record

Date(y/m/d)	Rev.	Description	Note	Page
2002/06/14	0	Revised Contents		

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1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	128 dots *64 dots
LCD Type	STN, Blue, Transmissive,negative, Extended Temp.
Driver Type	1/64 Duty , 1/9 Bias
Viewing Direction	6 O'clock
Backlight	White LED B/L
Weight	-
Other	-

1.2 Mechanical Specifications

Item	Standard Value					
Outline Dimension	93.0(L) *70.0(W)*14.0 max (H)	mm				
Viewing Area	72.0(L)*40.0(W)	mm				
Active Area	66.52(L)*33.24(W)	mm				
Dot Size	0.48(L)*0.48(W)	mm				
Dot Pitch	0.52(L)*0.52(W)	mm				

1.3 Absolute Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Power supply Voltage	VDD	-	-0.3	7.0	V
LCD drive Supply voltage	VDD-VEE	-	VDD-19.0	VDD+0.3	V
Input voltage	VIN	-	-0.3	VDD+0.3	V
Operating temperature	TOPR	-	-20	70	°C
Storage temperature	TSTG	-	-30	80	°C
Humidity*1	HD	-	-	90	%RH

1.4 DC Electrical Characteristics

 V_{DD} = 5.0 V ± 10% , V_{SS} = 0V , Ta = 25°C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V_{DD}	-	4.5	5.0	5.5	V
"H" Input Voltage	V _{IH}	-	0.7 V DD	1	Vdd	V
"L" Input Voltage	V_{IL}	-	0	1	0.3 VDD	V
"H" Output Voltage	V _{OH}	IOH=-0.1mA	2.4	ı	-	V
"L" Output Voltage	V_{OL}	IOL=0.1mA	-	ı	0.4	V
Supply Current	l _{DD}	$V_{DD} = 5.0 \text{ V}$	-	2.5	5.0	mA
		V _{DD} - V _O (-20°C)	ı	ı	-	
LCD Driver Voltage	V_{OP}	V _{DD} - V _O (25°C)	-	14	-	V
		V _{DD} - V _O (70°C)	-	-	-	

1.5 Optical Characteristics

1/64 Duty, 1/9 Bias, VOP = 14 V, $Ta = 25^{\circ}\text{C}$

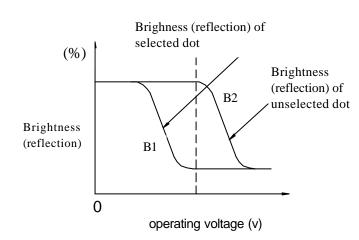
Item	Symbol	Conditions	Min.	Тур.	Max.	Reference
View Angle	θ	C <u>≥</u> 2.0,Ø=0°	50°	ı	ı	Notes 1 & 2
Contrast Ratio	С	θ=25°, Ø= 0°	3	7	ı	Note 3
Response Time(rise)	tr	θ= 25°, Ø= 0°		150 ms	1	Note 4
Response Time(fall)	tf	θ= 25°, Ø= 0°	-	300 ms	-	Note 4

Note 1: Definition of angles θ and \emptyset

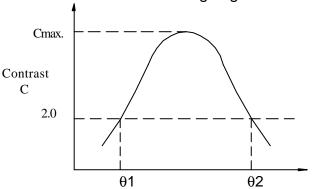
Light (when reflected) $z (\theta=0^{\circ})$ Sensor $Y'(\emptyset=180^{\circ})$ LCD panel X' Z'Light (when transmitted) $Y(\emptyset=0^{\circ})$ $(\theta=90^{\circ})$

Note 3: Definition of contrast C

C = Brightness (reflection) of unselected dot (B2) Brightness (reflection) of selected dot (B1)



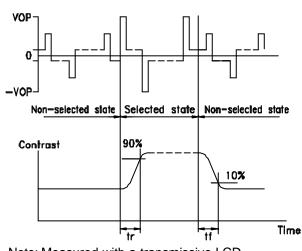
Note 2: Definition of viewing angles θ 1 and θ 2



viewing angle θ (\emptyset fixed)

Note: Optimum viewing angle with the naked eye and viewing angle θ at Cmax. Above are not always the same

Note 4: Definition of response time



Note: Measured with a transmissive LCD panel which is displayed 1 cm²

 V_{OPR} : Operating voltage f_{FRM} : Frame frequency t_{r} : Response time (rise) t_{f} : Response time (fall)

1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	72	mA
Reverse Voltage	VR	Ta =25°C	-	5	V
Power Dissipation	РО	Ta =25°C	-	0.29	W
Operating Temperature	T _{OP}	-	-20	70	°C
Storage Temperature	T _{ST}	-	-30	80	°C

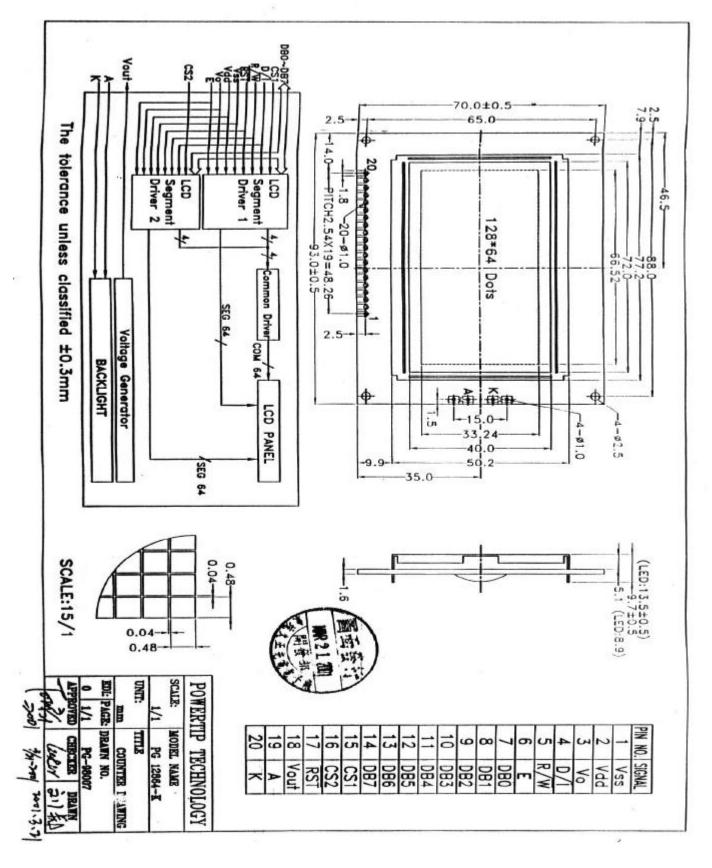
Electrical Ratings

Ta =25°C

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Forward Voltage	VF	IF=60 mA	3	3.3	4	V	
Reverse Current	IR	VR=5V	-	-	0.15	mA	
Luminous Intensity (with LCD, Dots Off)	IV	IF=60 mA	160	245	-	cd/m ²	
Wavelength	р	IF=60 mA	-	White	1	nm	
Color	White						

2. MODULE STRUCTURE

2.1 Counter Drawing



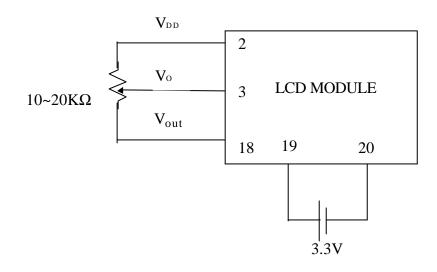
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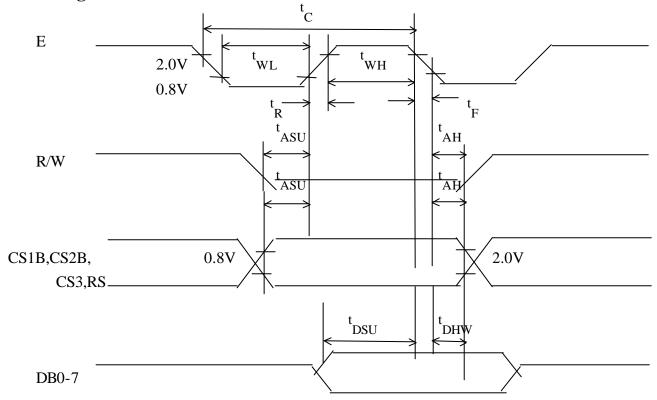
2.2 Interface Pin Description

Pin No.	Symbol	Function
1	Vss	Signal ground (GND)
2	Vdd	Power supply for logic (VDD> VSS)
3	Vo	Operating voltage for LCD (variable)
4	D/ I	Register selection input High =Data register Low =Instruction register (for write) Busy flag address counter (for read)
5	R/W	R/W signal input is used to select the read/write mode High =Read mode, Low =Write mode
6	E	Start enable signal to read or write the data
7-14	DB0-DB7	Data bus
15	CS1	Chip enable for D2 (segment 1 to segment 64)
16	CS2	Chip enable for D3 (segment 65 to segment 128)
17	RST	Reset signal
18	Vout	Negative voltage power supply
19	A	Power supply for LED backlight (+)
20	K	Power supply for LED backlight (-)

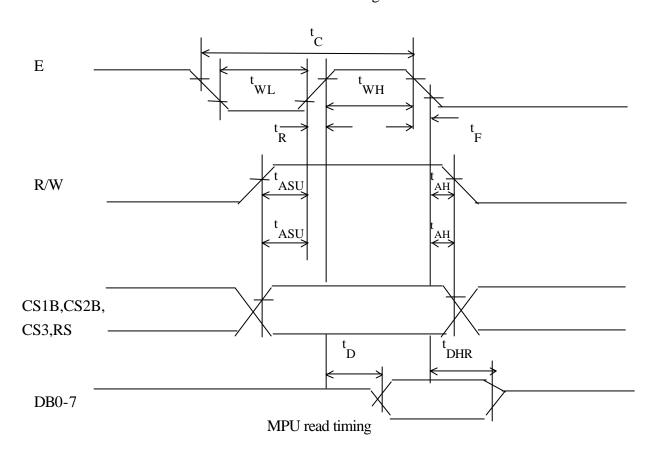
Contrast Adjust



2.3 Timing Characteristics



MPU write timing



Characteristic	Symbol	Min.	Тур	Max	Unit
E Cycle	tC	1000	-	-	ns
E High Level Width	tWH	450	-	-	ns
E Low Level Width	tWL	450	-	-	ns
E Rise Time	tR	-	-	25	ns
E Fall Time	tF	-	-	25	ns
Address Set-Up time	tASU	140	-	-	ns
Address Hold Time	tAH	10	-	-	ns
Data Set-Up Time	tDSU	200		-	ns
Data Delay Time	tD	-	-	320	ns
Data Hold Time (Write)	tDHW	10	-	_	ns
Data Hold Time (Read)	tDHR	20	-	-	ns

2.4 Display command

Instructions	D/I	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Functions
											Controls the display on or
Display on/off	L	L	L	L	Н	Н	Н	Н	Н	L/H	Off. Internal status and
											display RAM data is not
											affected.
											L: OFF , H: ON
Set address	L	L	L	Н			Y ad	dress			Sets the Y address in the Y
(Y address)							(0~	63)			address counter.
Set Page	L	L	Н	L	Н	Н	Н		Page		Sets the X address at the
(X address)									(0-7)		X register.
Display Start Line	L	L	Н	Н		D	isplay	start lir	ne		Indicates the display data
(Z address)							(0~	63)			RAM displayed at the top of
											the screen.
	L	Н	В	L	О	R	L	L	L	L	Reads status.
			U		N	Е					BUSY H: In operation
			S		/	S					L : Ready
Status Read			Y		О	Е					ON/OFF H : Display OFF
					F	Т					L : Display ON
					F						RESET H : Reset
											L : Normal
											Writes data (DB0:7) into
											display data RAM. After
Write Display	Н	L	Write Data					writing instruction, Y			
Data								address is increased by 1			
									automatically.		
											Reads data (DB0:7) from
Read Display Data	Н	Н				Read	Data				display data RAM to the
											data bus.

Detailed Explanation

Display On/Off

	RS	R/W	DB7	• • • • • •						.DB0)
Code	0	0	0	0	1	1	1	1	1	D	

The display data appears when D is 1 and disappears when D is 0. Though the data is not on the screen with D=0, it remains in the display data RAM. Therefore, you can make it appear by changing D=0 into D=1.



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Display Start Line (Z Address)

	RS	R/W	DB	7						.DB0
Code	0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z address(AC0-AC2) of display data RAM is set in the display start line register and display at the top of the screen. When the display duty cycle is 1/64 or others(1/32-1/64), the data of total line number of LCD screen, form the line specified by display start line instruction, is displayed. See figure 1.

Set page (X address)

	RS	R/W		2 ′/						DB0
Code	0	0	1	0	1	1	1	AC2	AC1	AC0

X address (AC0-AC2) of the display data RAM is set in the X address register. Writing or reading to or from MPU is executed in this specified page until the next page is set. See figure 2.

Set Adress (Y Address)

	RS	R/W	/ DI	37						DB0	_
Code	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	

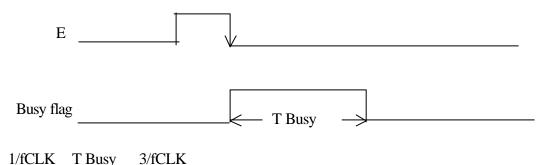
Y address(AC0-AC5) of the display data RAM is set in the Y address Counter. An address is set by instruction and increased by 1 automatically by read or write operation of display data.

Status Read

]	RS I	R/W	DB7							DB0
Code	1	0	BUSY	0	ON/OFF	REST	0	0	0	0

• Busy

When busy is 1, the Chip is executing internal operation and no instructions are accepted When busy is 0, the Chip is read to accept any instructions.



THEET TEASY SHEET

• ON/OFF

When on/off is 1, the display is OFF.

When on/off is 0, the display is ON.

• RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions except status read can be accepted.

When RESET is 0,initializing has finished and the system is in the usual operation condition.

Write Display Data

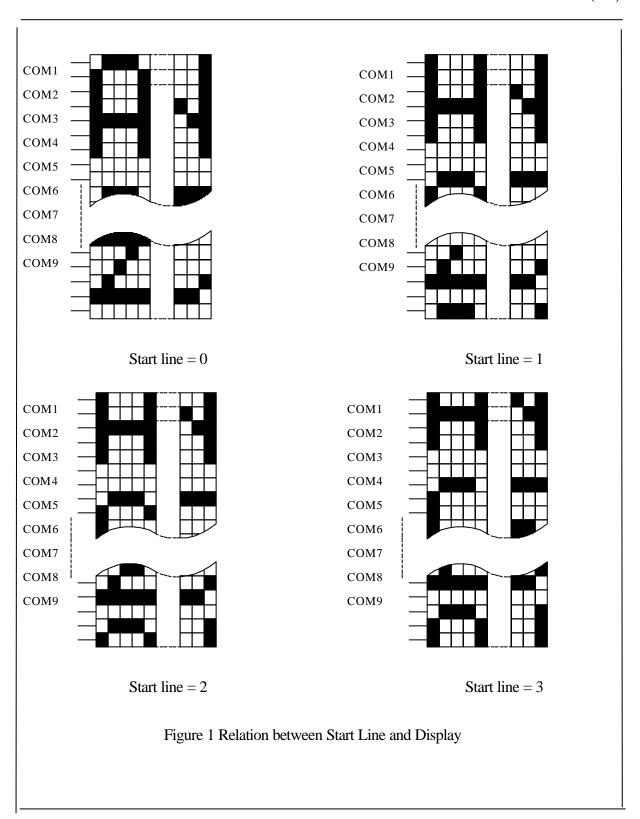
	RS	R/W	DB7							DB0
Code	0	1	D7	D6	D5	D4	D3	D2	D1	D0

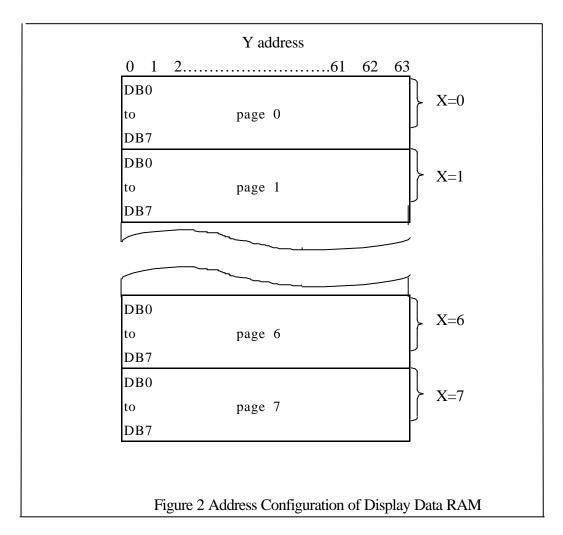
Write data(D0-D7)from the display data RAM. After writing instruction, Y address is increased by 1 automatically.

Read Display Data

	R/W	D/I	DB7						l	DB0
Code	1	1	D	D	D	D	D	D	D	D

Reads data(D0-D7) from the display data RAM. After reading instruction, Y address is increased by lautomatically





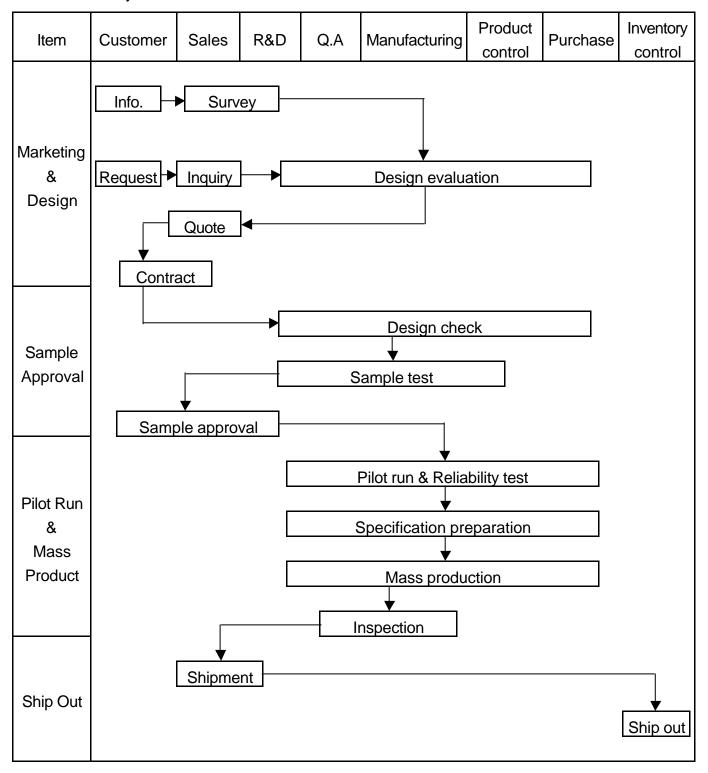
Note: "128*64" consist of 2 "64*64"

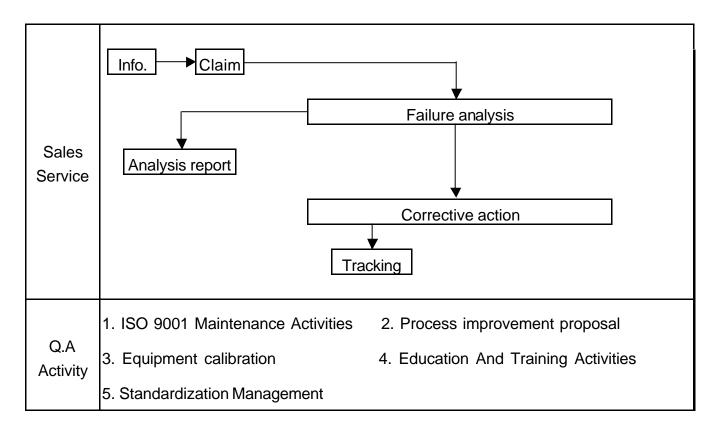
CS1⇒ Chip enable for left 64*64 (segment1 to segment 64)

CS2⇒ Chip enable for right 64*64 (segment 65 to segment 128)

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart





3.2 Inspection Specification

Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level .

Equipment: Gauge, MIL-STD, Powertip Tester, Sample,

IQC Defect Level: Major Defect AQL 0.65; Minor Defect AQL 1.0.

FQC Defect Level: 100% Inspection.
OUT Going Defect Level: Sampling.

Specification:

ΝO	Item	Specification	Judge	Level
1	Part Number	Inconsistent with the P/N on the flow chart of production	N.G.	Major
2	Quantity	Inconsistent Q'TY with the flow chart of production	N.G.	Major
	Ele etue ui e	Display short	N.G.	Major
	Electronic	Missing line	N.G.	Major
3	characteristics	Dot missing A > 1/2 Dot size	N.G.	Major
	A=(L + W) ÷ 2	No function	N.G.	Major
	7(2.77).2	Out put data error	N.G.	Major
		Material difference with flow chart	N.G.	Major
	A	LCD Assembled in opposite direction	N.G.	Major
	Appearance	Bezel assembled in opposite direction	N.G.	Major
	A=(L + W) ÷ 2	Shadow within LCD V./A + 1.0 mm	N.G.	Major
4	 	Dirty particle A > 0.4 mm	N.G.	Minor
7	Dirty particle (Include scratch, bubble)	Dirty particle length > 3.0mm And 0.01mm < Width 0.05mm (Width > 0.05mm Measure by area)	N.G.	Minor
	Sciator, bubble)	Without protective film	N.G.	Minor
		Conductive rubber over bezel	N.G.	Minor
		Burned PCB	N.G.	Major
		Green paint stripped & visible circuit A > 1.0mm (Finish coat not counted in)	N.G.	Minor
	PCB Appearance	A particle across the circuit	N.G	Minor
5		Circuit split > 1/2 Circuit width	N.G	Minor
	A=(L + W) ÷ 2	Any circuit risen	N.G	Minor
		0.2mm < Tin ball area A 0.4mm And Q'TY > 4 Pieces	N.G	Minor
		Tin ball area A > 0.4mm	N.G	Minor

NO	Item	Specification	Judge	Level
		Too soft: Shape by touch changed	N.G.	Major
	Molding	Insufficient epoxy: IC circuit or IC pad visible	N.G.	Minor
6	appearance A=(L + W) ÷ 2	Excessive epoxy: Diameter > 20mm Or High > 2.5mm	N.G.	Minor
		Pin hole through to IC and A > 0.2mm	N.G.	Minor
		Angle between frame and TAB > 45 +10	N.G.	Minor
7	Bezel appearance	Electroplate strip A > 1.0mm (Top view only)	N.G.	Minor
'	A=(L + W) ÷ 2	Rust (Top view only)	N.G.	Minor
		Crack	N.G.	Minor
	Dooldiaht olootrio	Error backlight color	N.G.	Major
	Backlight electric characteristics	No function	N.G.	Major
8	Characteristics	Any LED dot no function	N.G.	Major
	A=(L + W) ÷ 2	PIN soldering without tin A > 1/2 solder pad	N.G.	Minor
		Solder PIN high > 1.5mm	N.G.	Minor
9	LCD Appearance A=(L+W)÷2	Polarize rise over V/A	N.G.	Minor
		Components mark unclearly	N.G.	Minor
		Components' distance more than 0.7mm firm the PCB	N.G.	Minor
10	Assembly parts A=(L + W) ÷ 2	Error position ,not in center D > 1/4W D D Pad	N.G.	Minor
		Non- solder area > Twice solder area	N.G.	Minor
		Flux area A > 1/4 solder area	N.G.	Minor
		Component broken	N.G.	Minor

4. RELIABILITY TEST

4.1 Reliability Test Condition

NO	ltem	Test Co	ondition	Applicable Standard
1	High Temperature Storage	Storage At 80 ± 2 Surrounding Temperate At Normal Condition 4h	ure , Then Storage	MIL-202E
2	Low Temperature Storage	Storage At -30 ± 2 Surrounding Temperate At Normal Condition 4h	ure, Then Storage	MIL-202E
3	High Temperature Humidity Storage	Surrounding Tempera At Normal Condition 4 fail in this environment	4hrs .(Polarizer may t). 40 ± 2 , 90~95%RH rature, Then Storage	MIL-202E
4	Temperature Cycling	-20 25 (30Mins) (5Mins)	70 25 (30Mins) (5Mins) Cycle	MIL-202E
5	Vibration	10~55Hz (1 M X,Y And Z Direction	,	MIL-202E
6	Drop Test	Packing Weight (Kg) 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454	Drop High (Cm) 122 76 61 46	MIL-810E

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,
 do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25 \pm 5 and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.